1/5 AUS920030654US1

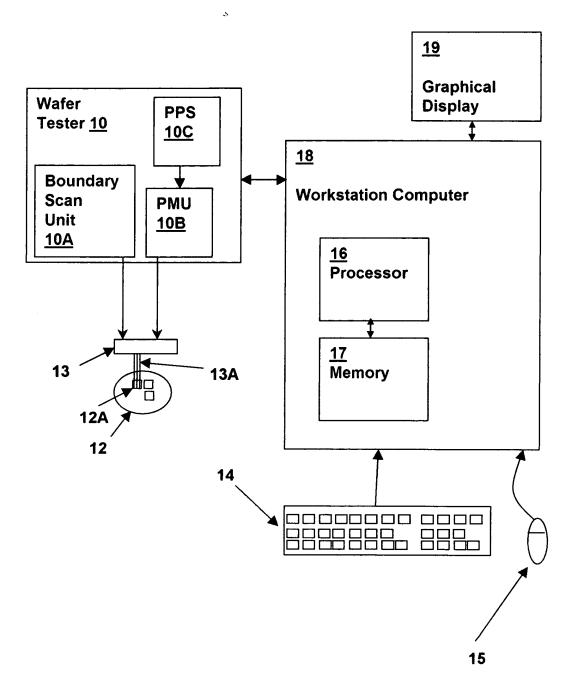


Fig. 1

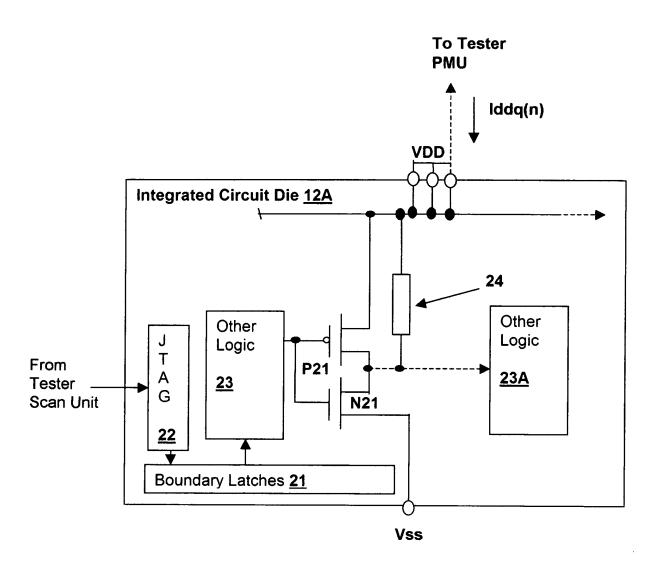
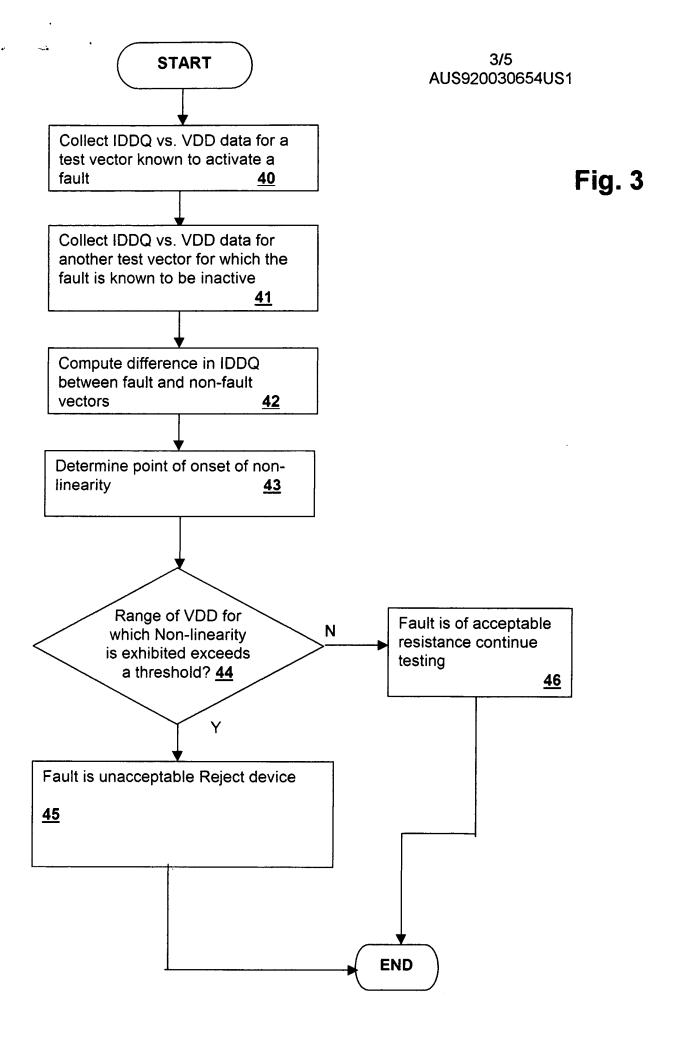


Fig. 2



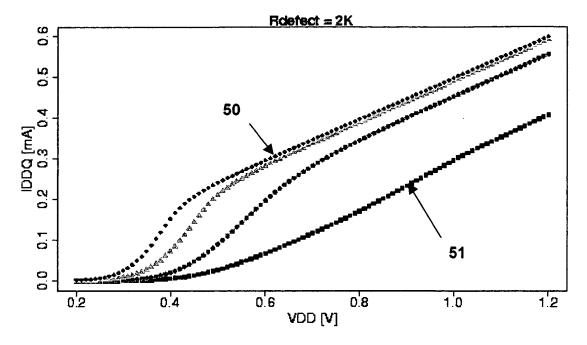


Fig. 4A

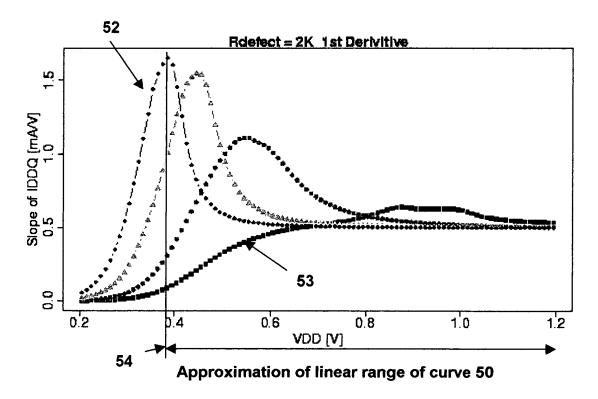


Fig. 4B

Excess delay versus nonlinearity onset voltage

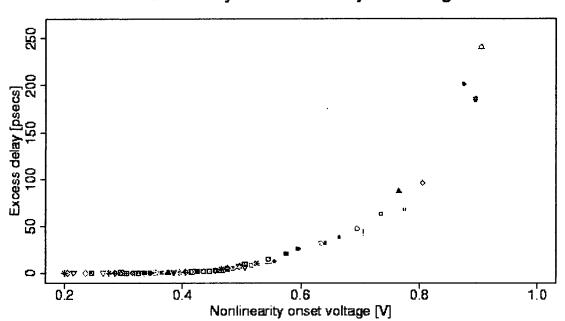


Fig. 4C